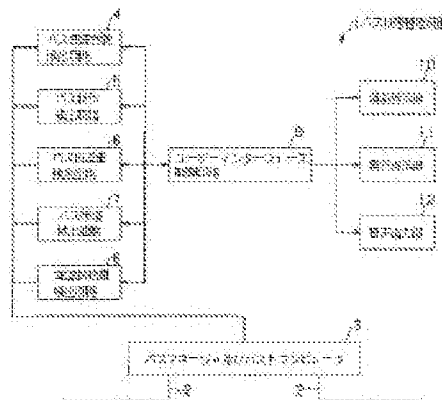


BUS STATE MANAGEMENT DEVICE**Publication number:** JP9331342 (A)**Publication date:** 1997-12-22**Inventor(s):** KOBAYASHI HIROAKI**Applicant(s):** TOKYO SHIBAURA ELECTRIC CO; TOSHIBA AVE KK**Classification:**- international: **G06F13/00; H04L12/40; G06F13/00; H04L12/40; (IPC1-7): H04L12/40; G06F13/00**

- European:

Application number: JP19960149300 19960611**Priority number(s):** JP19960149300 19960611**Abstract of JP 9331342 (A)**

PROBLEM TO BE SOLVED: To prevent an operation fault or the like due to excess of the number of connected nodes. **SOLUTION:** A bus connection node number detection circuit 4 detects the number of nodes connected to a bus 2 based on information from a bus manager and host computer 3. Furthermore, a bus operation device 5 detects a fault in the bus, a bus transmission amount detection circuit 6 detects a transmission rate of the bus, a bus frequency band detection circuit 7 detects the presence of the transfer rate between nodes and that of isochronous transfer and a power supply detection circuit 8 obtains information relating to a power supply. The information is informed to a user by a user interface control circuit 9 by means of a liquid crystal display section 10, a light emission display section 11 and a voice output section 12 or the like. Thus, the user avoids an operation fault or the like.

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